

3971 U.S. PTO  
10/06/151  
01/29/02  
2003-883

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10067151	01/29/2002	716	16	2825	DO

\*\*APPLICANTS: Rupp Charles; Garverick Timothy; Arnold Jeffrey;

\*\*CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/265,303 01/30/2001  
YES TD

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		STRE-003/01US	
Verified and Acknowledged Examiners's initials TD			
TITLE : Design methodology for merging programmable logic into a custom IC			
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)			

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	Total Claims
			Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drawn	Figs Drawn
			Print Fig
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner	Application Examiner
		<b>PREPARED FOR ISSUE</b>	
<b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH: ☐ DISK (CRF) ☐ CD-ROM  
(Attached in pocket on right inside flap)